

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	76677	clock near5 (frequency or speed)	USPA T; US-P GPUB	2004/03/0 9 14:38	
2	BRS	L2	33194 7	(vary\$3 or chang\$3 or reduc\$3) near5 (frequency or speed)	USPA T; US-P GPUB	2004/03/0 9 14:40	
3	BRS	L3	31208	1 and 2	USPA T; US-P GPUB	2004/03/0 9 14:40	
4	BRS	L4	4590	processor near3 core	USPA T; US-P GPUB	2004/03/0 9 14:40	
5	BRS	L5	7774	execution adj unit	USPA T; US-P GPUB	2004/03/0 9 14:40	
6	BRS	L6	455	3 and 5	USPA T; US-P GPUB	2004/03/0 9 14:41	
7	BRS	L7	109	4 and 6	USPA T; US-P GPUB	2004/03/0 9 14:41	
8	BRS	L8	12811 6	power near3 consum\$5	USPA T; US-P GPUB	2004/03/0 9 14:42	
9	BRS	L9	53	7 and 8	USPA T; US-P GPUB	2004/03/0 9 14:42	
10	BRS	L10	1653	713/300.ccls. 713/320.ccls.	USPA T; US-P GPUB	2004/03/0 9 14:43	
11	BRS	L11	896	713/322.ccls. 713/323.ccls.	USPA T; US-P GPUB	2004/03/0 9 14:43	
12	BRS	L12	2285	10 or 11	USPA T; US-P GPUB	2004/03/0 9 14:43	
13	BRS	L13	1371	713/500.ccls. 713/501.ccls. 713/601.ccls.	USPA T; US-P GPUB	2004/03/0 9 14:44	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	22	9 and (12 or 13)	USPA T; US-P GPUB	2004/03/0 9 15:22	
15	BRS	L15	31	9 not 14	USPA T; US-P GPUB	2004/03/0 9 15:22	
16	BRS	L16	13441	control\$3 near5 clock near5 (frequency or speed)	USPA T; US-P GPUB	2004/03/0 9 15:50	
17	BRS	L17	11635	processor same unit same clock	USPA T; US-P GPUB	2004/03/0 9 15:51	
18	BRS	L18	947	16 and 17	USPA T; US-P GPUB	2004/03/0 9 15:51	
19	BRS	L19	3488	12 or 13	USPA T; US-P GPUB	2004/03/0 9 15:51	
20	BRS	L20	175	18 and 19	USPA T; US-P GPUB	2004/03/0 9 15:52	
21	BRS	L21	165	20 not 9	USPA T; US-P GPUB	2004/03/0 9 15:52	
22	BRS	L22	23548 4	power near5 control\$3	USPA T; US-P GPUB	2004/03/0 9 15:53	
23	BRS	L23	109	21 and 22	USPA T; US-P GPUB	2004/03/0 9 15:53	